

L&L-I0225

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.	:	10/730,619	Conf. No.:	4277
Applicant	:	Burkhard Becker		
Filed	:	December 8, 2003		
Art Unit	:	2185		
Examiner	:	Yong Choe		
Title	:	Method and Circuit Configuration for Transmitting Data Between a Processor and a Hardware Arithmetic-Logic Unit		
Docket No.	:	L&L-I0225		
Customer No.	:	24131		

RESPONSE TO FINAL OFFICE ACTION

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated March 4, 2008 kindly consider the
following remarks:

Remarks/Arguments begin on page 2 of this paper.